

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S6	13	(( REGIST\$4 adj MEMORY adj MODULE ) and (enabl\$4 adj signal)) and (activ\$5 adj signal)) and (clock\$5 adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/18 02:17
S9	535	((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S11	92	((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) and "365"/\$7.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S15	32	(((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) not "365"/\$7.ccls.) not "711"/\$7.ccls.) not (((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) not "365"/\$7.ccls.) not "714"/\$7.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S16	1	"20020131305"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/07/12 06:56
S19	19	(( REGIST\$4 adj MODULE )) and (internal adj clock)) and pll	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S20	31	(( REGIST\$4 adj MODULE )) and reduce adj power adj consump\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S22	35	(( REGIST\$4 adj MODULE )) and reduce adj power ) and dram	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S25	71	((dram and register) and (flip-flop)) and synchroniz\$4 adj clock) and (reduce adj power)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/07/12 06:56
S26	137	REGIST\$4 adj MEMORY adj MODULE	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S27	67	( REGIST\$4 adj MEMORY adj MODULE ) and (enabl\$4 adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11

S28	61	( REGIST\$4 adj MEMORY adj MODULE ) and (enabl\$4 adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S29	14	(( REGIST\$4 adj MEMORY adj MODULE ) and (enabl\$4 adj signal)) and (activ\$5 adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S30	52	(( REGIST\$4 adj MEMORY adj MODULE ) and (enabl\$4 adj signal)) and (clock\$5 adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S31	22400	MEMORY adj MODULE	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S32	3382	(MEMORY adj MODULE ) and (enabl\$4 adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S33	384	((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S34	284	(((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) not "365"/\$7.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S35	236	((((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) not "365"/\$7.ccls.) not "711"/\$7.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S36	246	((((((MEMORY adj MODULE ) and (enabl\$4 adj3 signal)) and (activ\$5 adj signal)) and clock adj signal) not "365"/\$7.ccls.) not "714"/\$7.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S37	1196	( REGIST\$4 adj MODULE )	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S38	90	(( REGIST\$4 adj MODULE )) and (internal adj clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S39	64	(( REGIST\$4 adj MODULE )) and reduce adj power	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11

S40	22427	dram and register	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S41	3965	(dram and register) and (flip-flop)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S42	4	"6731548"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S43	2	"20030227798"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/11 20:11
S44	4	"6731548"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/13 21:25
S45	2	"20030227798"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/13 21:25